



OR

- 8 a. Interface two  $4k \times 8$  EPROMS and two  $4k \times 8$  RAM chips with 8086. (06 Marks)  
b. Interface eight seven segment display using 8255 with 8086. (06 Marks)  
c. Draw the timing diagram for  $\overline{RQ}$  /  $\overline{GT}$  for maximum mode. (04 Marks)

Module-5

- 9 a. Draw and discuss the interface between 8086 and 8087. (08 Marks)  
b. Explain the following keyboard handling INT21 DOS function : (03 Marks)  
i) Function 01h ii) Function 08h.  
c. Write an ALP to interface stepper molar to 8086. (05 Marks)

OR

- 10 a. Differentiate between : (06 Marks)  
i) Harvard and Von Neuman Architecture ii) RISC and CISC Architecture.  
b. Explain the significance of different bits of control word, Register format of 8253/54. (06 Marks)  
c. Write a program to generate triangular wave using DAC 0800. (04 Marks)

\*\*\*\*\*